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**PATENT APPLICATION**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Osamu UENO et al.

Group Art Unit: 2841

Application No.: 09/805,250

Examiner: T. DINH

Filed: March 14, 2001

Docket No.: 108910

For: CIRCUIT BOARD DEVICE AND DESIGN SUPPORT DEVICE

**APPLICANTS' SEPARATE RECORD OF  
PERSONAL AND TELEPHONE INTERVIEWS**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Applicants appreciate the courtesies shown to Applicant's representative during the January 18, 2006 personal interview and January 20, 2006 telephone interview. The following is Applicants' separate record of the interviews.

During the January 18 personal interview, Applicants' representative explained that the outstanding objection and rejections should be withdrawn based on Applicant's November 14, 2005 Amendment. Examiner Dinh agreed that Applicant's arguments may be sufficient to overcome the outstanding rejections, but asserted that at least claim 1 may be unpatentable over U.S. Patent 5,587,887 to Price et al. (hereinafter "Price") in view of U.S. Patent 5,898,576 to Lockwood et al. (hereinafter "Lockwood").

Examiner Dinh offered to allow the Application if claim 1 were canceled and claims 2, 3, 5, 6, 8, 21, 25 and 26 were amended to depend from claim 9. Examiner Dinh offered to make the suggested amendments by Examiner's Amendment. Applicants' representative agreed to consider the offer.

During the January 20 telephone interview, Applicants' representative proposed canceling claim 1, slightly amending claim 9, amending claims 2, 3, 5, 6, 8, 21, 25 and 26 to depend from claim 9, and reintroducing canceled claims 15-20, 22-24, 27, and 28 such that they also depended from claim 9. Examiner Dinh agreed to canceling claim 1, slightly amending claim 9, amending claims 2, 3, 5, 6, 8, 21, 25 and 26 to depend from claim 9, but was unwilling to add new claims representing canceled 15-20, 22-24, 27, and 28 and depending from claim 9.

Applicants accepted Examiner Dinh's offer. Accordingly, by Examiner's Amendment, the claims will be amended as set forth in the attached Appendix and allowed.

Respectfully submitted,



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JAO:JOC/tea

Attachment:  
Appendix

Date: January 23, 2006

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1. (Canceled)
2. (Currently Amended) The circuit board device according to ~~claim 1~~ claim 9, further comprising a dielectric layer, wherein the power supply region and the ground region that are adjacent are formed in different layers with the dielectric layer interposed therebetween and substantially oppose one another and overlap.
3. (Rejoined/Currently Amended) The circuit board device according to ~~claim 1~~ claim 9, further comprising a circuit, which has a plurality of via holes that forms a parallel circuit, formed adjacent to the terminal portion, and the terminal element is connected between the layer via the parallel circuit.
4. (Canceled)
5. (Currently Amended) The circuit board device according to ~~claim 1~~ claim 9, wherein the terminal element includes a capacitor.
6. (Currently Amended) The circuit board device according to ~~claim 1~~ claim 9, wherein the terminal element includes a resistor and a capacitor which are series-connected.
7. (Canceled)
8. (Currently Amended) The circuit board device according to ~~claim 1~~ claim 9, wherein an impedance  $Z_r$  of the terminal element is set such that the characteristic impedance  $Z_e$  and the impedance  $Z_r$  of the terminal element satisfy a relationship  $0.1 \leq Z_r/Z_e \leq 10$ .
9. (Currently Amended) A circuit board device having a power supply region ~~divided into two or more power supply regions by a slit~~ and a ground region adjacent to the power supply region, wherein, when the power supply is divided into two or more power supply regions by a slit, at least one region of said two or more power supply regions and the ground region that are adjacent is in the shape of a track having a length that is larger than its width, a terminal element having an impedance that is substantially equal to a characteristic impedance between said at least one region and said ground region is connected between said

power supply region and said ground region at a terminal end of said at least one region,  
wherein the terminal element includes a resistor and a capacitor which are series-connected, a  
relationship between the characteristic impedance  $Z_e$  and a resistance  $R$  of the resistor  
satisfies  $(Z_e/5) \leq R \leq (5 \cdot Z_e)$ , and a capacity  $C$  of the capacitor satisfies  $C \geq 1/(10\pi \cdot f_{\min} \cdot Z_e)$ ,  
where  $f_{\min}$  is a lower limit of a radiation noise frequency of a reduction target.

10-14. (Canceled).

15-20. (Canceled)

21. (Currently Amended) The circuit board device according to ~~claim 1~~ claim 9,  
wherein the slit has a T-shape.

22-24. (Canceled)

25. (Currently Amended) The circuit board device according to ~~claim 1~~ claim 9,  
wherein terminal element is connected between said power supply region and said ground  
region only at said terminal end of said at least one region.

26. (Currently Amended) The circuit board device according to ~~claim 1~~ claim 9,  
wherein an outer periphery of said power supply region and said ground region is free from  
connection by said terminal element.

27-28. (Canceled)